

Notice of References Cited	Application/Control No. 10/072,798	Applicant(s)/Patent Under Reexamination TSAI ET AL.	
	Examiner DuyVu n Deo	Art Unit 1765	Page 1 of 1

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era, Vol. 1, 1986, pages 529, 539.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.